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for , Digital , VLSI , IC , Design for EE423 at Oregon Tech.

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Jan Rabaey, UC Berkeley, The road towards xG -- A Swarm Perspective von Johannesburg Summit vor 6 Jahren 24 Minuten 1.188 Aufrufe <http://johannesbergsummit.com> Watch an interview with Jan , Rabaey , , made after the talk at <http://youtu.be/pP74O5Y7NPY> ...

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CASS Talks 2020 - Sergio Bampi, UFRGS, Brazil - October 16, 2020 von IEEE CASS Rio Grande do Sul Chapter vor 1 Monat 1 Stunde, 28 Minuten 38 Aufrufe CASS Talks 2020 - October 16, 2020 Maximum CMOS Energy Efficiency for IoT Devices: The case for Near-Threshold Logic and ...

[VLSI L8A Arithmetic Circuits](#)

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Signal Design and VLSI Design workshop arranged for teachers.